APPLICATION FOR UNITED STATES PATENT

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INVENTION: Arbitrary and Expandable High-Precision Datatype and Method of Processing
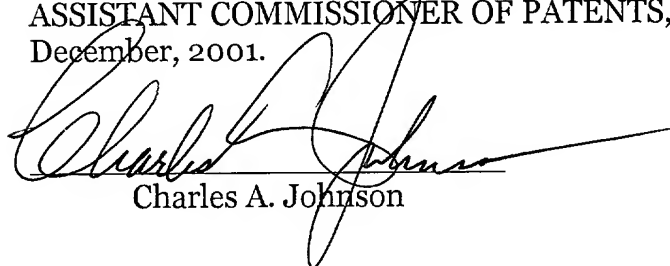
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SPECIFICATION

ARBITRARY AND EXPANDABLE HIGH-PRECISION DATATYPE
AND METHOD OF PROCESSING

5   ## COPYRIGHT NOTICE

FIELD OF THE INVENTION

The present invention generally relates to numeric datatypes in programming languages, and more particularly to a numeric datatype that is
15   expandable to a high level of precision.

BACKGROUND OF THE INVENTION

High-precision arithmetic is useful in computer programs that are directed to solving problems in certain disciplines. Similar classes of applications may
20   also require arbitrary size arithmetic.

While arithmetic data sizes of 32 or 64 bits are sufficient for many applications, some highly-specialized applications may require hundreds or thousands of bits of precision. When dealing with very large numbers, relatively small - yet significant - changes will be lost with limited precision. For example,
25   when dealing with a double precision datatype, $10^{30} + 1$ billion $= 10^{30}$. In other words, 1 billion is lost in the computation. Many supercomputing application such as physics modeling and numerical methods suffer from accumulated errors due to limited-precision rounding. In another example, some supercomputing applications, such as astrophysics and encryption, deal with
30   extremely large numbers. An "int" datatype typically offers 32 bits of size and supports values in the range of 4 billion, and the "double" datatype offers 1024

bits of size and supports values in the range of 10^{400}. For these applications, the range provided by "int" and "double" datatypes may be insufficient.

Special libraries have been developed to accommodate high-precision arithmetic. An example library of functions that supports arbitrary precision arithmetic is the GNU MP (GMP) library. The GMP library provides special functions that are available for use in manipulating high-precision data. To arithmetically manipulate high-precision data with the GMP library, a user must learn the names, behavior, and parameters of various specialized functions provided in the library. Thus, there is a significant learning curve that accompanies use of the GMP library. Failure to use the library correctly can lead to program failure or unexpected results.

A method and apparatus that addresses the aforementioned problems, as well as other related problems, are therefore desirable.

SUMMARY OF THE INVENTION

In various embodiments, the invention provides a method and apparatus for processing numerical values of arbitrary and expandable precision. In various embodiments, the invention provides an arbitrary and expandable high-precision datatype for declaring "large-integer" data. The datatype encapsulates large-integer data and associated operations. The large-integer data has runtime expandable precision, and the operations perform functions on large-integer data and system integer data in a manner that is functionally equivalent to corresponding language-provided integer operations. The large-integer data is interoperable with system integer data. A user is unburdened with special commands and syntax by virtue of overloading the language-provided integer operations with the operations of the large-integer datatype.

The above summary of the present invention is not intended to describe each disclosed embodiment of the present invention. The figures and detailed description that follow provide additional example embodiments and aspects of the present invention.

BRIEF DESCRIPTION OF THE DRAWINGS

Other aspects and advantages of the invention will become apparent upon review of the Detailed Description and upon reference to the drawings in which:

FIG. 1 is a block diagram that illustrates the relationship between a user's view of a numeric value that is of a LargeInt datatype and the underlying storage of the value in accordance with an example embodiment of the invention;

FIG. 2 is a flow diagram that illustrates the process flow of a memory manager for IntNodes, of which the LargeInt datatype is internally composed;

FIG. 3 is a block diagram that illustrates the relationship between the sizes of a system byte, an IntNode, and a system integer;

FIG. 4A is a block diagram that illustrates converting between a character array and a LargeInt variable where each character digit is represented with fewer bits than are available in an IntNode;

FIG. 4B is a block diagram that illustrates relative bit positions in converting between a character array and a LargeInt variable where each character digit is represented with more bits than are available in an IntNode; and

FIG. 4C is a block diagram that illustrates relative bit positions in converting between a system integer and a LargeInt variable where, for example, the number of bits used to represent a system integer is equal to the number of bits in three IntNodes.

DETAILED DESCRIPTION

The present invention is directed to a datatype ("LargeInt") that supports values of arbitrary and expandable precision. The precision of LargetInt variables is limited only by the available computing resources, for example, RAM. In addition, the size of a LargeInt variable can be fixed either at compile time or at runtime. A user program manipulates LargeInt variables using the same functions as are available with standard precision or double precision integers. The underlying methods that manipulate LargeInt variables in response to user-programmed functions scale with the precision of the variable, and the methods efficiently manage the memory allocated to LargeInt variables.

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　　　　FIG. 1 is a block diagram that illustrates the relationship between a user's view of a numeric value that is of a LargeInt datatype and the underlying storage of the value in accordance with an example embodiment of the invention. Block 102 illustrates the numeric value as viewed by a user, and block 104 illustrates the

5　　memory allocated for storage of the numeric value of block 102. It will be appreciated that for ease of description, the numeric value shown in blocks 102 and 104 is in hexadecimal format.

　　　　Each of blocks 106-114 is referred to as an *IntNode* and stores n bits of the numeric value (n is user-customizable; 16 bits in the example), with the least

10　　significant bits (LSBs) in IntNode 106 and the most significant bits (MSBs) in IntNode 114. In one embodiment, the IntNodes are stored as a doubly-linked list for ease of traversal. Each variable of LargeInt type has an associated sign bit, and the sign bit of the numeric value is stored in block 120.

　　　　Usage of the LargeInt datatype in a program is straightforward and does

15　　not require knowledge of the underlying storage scheme or methods. For example, the following code fragment shows manipulation of variable of a conventional integer datatype.

```
        int X, Y = 5;
        cin >> X;
20      if (X < 3)
                return X + 0x7FFF;
        else
                return X % (Y << 02);
```

The following code fragment shows the variables X and Y declared of the type

25　　LargeInt. Note that from the programmer's view the manipulation of the variables remains the same.

```
        LargeInt X, Y = 5;
        cin >> X;
        if (X < 3)
30              return X + 0x7FFF;
        else
                return X % (Y << 02);
```

By implementing the LargeInt datatype in C++, normal integer operators are overloaded with functions that manipulate variables of the LargeInt datatype. This allows the LargeInt datatype to operate transparently relative to the

5   programmer. In addition, LargeInt data can be manipulated in combination with all standard datatypes, including all signed and unsigned integral types, floating point types, Boolean, and character arrays. Appendix A illustrates, in an example embodiment, encapsulation of the LargeInt datatype and how language provided operators are overloaded with LargeInt functions. The functions that operate on

10   LargeInt data manipulate LargeInt data as stored in IntNodes as compared to language-provided functions which operate on system integers. Those skilled in the art will appreciate that various modifications can be made to the header file of Appendix A to accommodate different or additional implementation requirements.

15   FIG. 2 is a flow diagram that illustrates the process flow for IntNode memory management for variables of the LargeInt datatype. The process is illustrated and described in terms of the life cycle of a variable of the LargeInt datatype (200). Block 202 commences allocation of a new or additional IntNodes to a variable, and block 204 commences deletion of one or more

20   IntNodes, for example, when fewer IntNodes are required or the variable is destroyed.

The "New IntNode" function is initiated when IntNodes are initially allocated for a LargeInt variable or when additional IntNodes are required for a variable. IntNodes are allocated from IntNode pool 206, which in one

25   embodiment is a list of unused IntNodes. If pool is not empty (step 208), an IntNode is removed from the pool (210) and returned to the calling routine. An example calling routine is an arithmetic function that is invoked to manipulate a LargeInt variable. If the node pool is empty, additional memory is allocated and new IntNodes are created in the IntNode pool (step 214).

30   The "Delete IntNode" function is initiated when a LargeInt variable requires fewer IntNodes or when a LargeInt variable is no longer needed. The unneeded IntNodes are returned (step 222) to the IntNode pool 206. When the

5

IntNode pool reaches a selected maximum size (step 224), IntNodes are removed from the pool (step 226), and the associated memory is deallocated. Control is then returned to the calling routine.

Custom memory management is achieved by overloading the C++ new and delete operators for IntNodes. By doing so, the underlying memory management of the IntNodes is transparent to the algorithms and functions that manipulate IntNodes.

FIG. 3 is a block diagram that illustrates the relationship between the sizes of a system byte 302, an IntNode 304, and a system integer 306. The number of bits in system byte 302 is defined by the system in which the present invention is used. For example, most systems operate with 8-bit bytes. The size of the IntNode must be a multiple of bytes, but the exact multiple can be varied by the user to optimize memory usage. The selected size of the IntNode is also influenced by the size of a system integer. The size of the system integer must be a multiple (> 1) of the size of the IntNode. This optimizes processor performance and register usage by making sure that the result of a multiplication of two IntNodes can fit in a system integer

FIGs. 4A, 4B, and 4C illustrate how operations on LargeInt variables are independent of the number of bits per byte, the number of bits per digit, and the number of bytes per integral types. "Digit" refers to base-n digits in a character array, where character arrays are used to serialize LargeInts, for user input and output, and as constants.

FIG. 4A is a block diagram that illustrates converting between a character array 352 and a LargeInt variable 354 where each character digit is represented with fewer bits than are available in an IntNode. Line 356 shows that the current bit in IntNode 358 (left-most bit) corresponds to and has the same value as the current digit bit in digit 360.

FIG. 4B is a block diagram that illustrates relative bit positions in converting between a character array 362 and a LargeInt variable 364 where each character digit is represented with more bits than are available in an IntNode. Line 366 shows that the current IntNode bit in IntNode 368 corresponds to and has the same value as the current digit bit in digit 370.

FIG. 4C is a block diagram that illustrates relative bit positions in converting between a system integer 380 and a LargeInt variable 382 where, for example, the number of bits used to represent a system integer is equal to the number of bits in three IntNodes. Two integers are shown to clarify what happens

5   at the boundary between integers. Because a "system integer" is exactly filled by a multiple of IntNodes, the integer is filled one whole IntNode at a time. This is in contrast to the IntNode/digit conversion, where there's no guarantee that digit and IntNode boundaries will be aligned, and therefore they must be filled bit by bit.

10   The LargeInt datatype supports fixed-bit data and constants that are larger than a system integer. Fixed-bit support operates by converting signed operands into unsigned operands of the specified bit length (e.g., using standard two's compliment), performing the normal operations, and then truncating the results to the specified bit length. For example, the example code fragment below

15   illustrates LargeInt support of arbitrary fixed-bit data.

```
LargeInt::SetFlag(LargeInt::FixedBit, 32);
LargeInt X = -1, Y = 0x7E00;
cout << hex << (X ^ Y);
LargeInt Z = 0xFFFFFFFF;
```
20
```
cout << Z + 1;
LargeInt::SetFlag(LargeInt::FixedBit, Off);
cout << Z + 1;
```

In this example implementation of fixed-bit support, the statement LargeInt::SetFlag(LargeInt::FixedBit, 32) signals to the methods that implement

25   operations on LargeInt variables that subsequent operations are to be performed using fixed-point operations. The statement, LargeInt::SetFlag(LargeInt::FixedBit, Off), turns off fixed-point operations on LargeInt variables. The output from the statement cout << hex << (X ^ Y) is 0xFFFF81FF, the output from the first statement cout << Z + 1 is 0x00000000,

30   and the output from the second statement cout << Z + 1 is 0x100000000.

From the foregoing example code fragment, it can also be seen that input and output of LargeInt data can be accomplished by reference to language-

provided input/output functions. For example, the LargeInt value Z + 1 is output with the cout function. This is done by overloading the << and >> operators with respect to the Standard C++ ostream and istream input/output classes. Input/output can also be accomplished by doing a standard (const char *) cast on

5   a LargeInt variable.

Constants that are larger than a system-provided integer are supported with the LargeInt datatype. LargeInt variables that are used as constants can be constructed from strings of characters. The following statement illustrates an implicit conversion of a character string to a LargeInt variable.

10

MyLargeInt += "123456789123456789123456789";

The statement below illustrates an explicit conversion from a character string to a LargeInt variable.

15

MyLargeInt = (LargeInt)" 0x123456789ABCDEF123456789ABCDEF" / 1234;

Another example of explicit conversion from a character string to a LargeInt variable is shown in the statement below.

20

MyLargeInt = LargeInt("b1101110101001001001001010010101010011001") << 2;

The methods that perform operations on LargeInt variables sometimes require storage for intermediate results that are accumulated in the computation.

25   Because LargeInt operations operate on lists of IntNodes instead of standard integers, it is important that the LargeInt operations efficiently manage storage for the intermediate results. One way in which the LargeInt operations efficiently manage storage for intermediate results is explained below. The example that follows illustrates handling of intermediate results.

30   The following code fragment illustrates a typical internal LargeInt operation.

    {

```
            LargeInt RetVal;
            // Create result of the operation
            // Store the result in RetVal
            return RetVal;
5       }
```

RetVal is a temporary variable because it is destroyed after the operation is complete. Another temporary variable will be created from the return value.

In the statement:

$$X = Y + Z + W;$$

10 a temporary variable ("temporary") is created to store the intermediate results of the return value of $(Y + Z)$, which is then provided as a parameter to:

```
            operator+ (temporary, W);
```

Another temporary variable is created from the return value of $(temporary + W)$ and is provided as a parameter to:

15
```
            X::operator=(temporary);
```

In all cases, the temporary variable has the same value as the return value, and the return value will be destroyed later. Instead of copying RetVal's list of IntNodes into the newly constructed temporary LargeInt variable, the list of IntNodes for RetVal is moved. This involves copying the LSB and MSB pointers

20 from RetVal to the temporary variable, and then setting RetVal's pointers to NULL during the construction (LargeInt(LargeInt)) or assignment (operator=(LargeInt)) of the new LargeInt variable. Thus, instead of an a cost $O(n)$ for traversing and copying the list of IntNodes from RetVal to the temporary (involves constructing n new IntNodes), and then traversing the list again in

25 order to destruct RetVal's n IntNodes, only two pointers are copied.

In order for the methods that perform operations on LargeInt variables to discern between temporary and non-temporary LargeInt variables, a class flag is set to indicate that the next parameter will be a temporary variable. For example, in the preceding code fragment, because the RetVal will be copied and then

30 destroyed, an internal flag can be set at the end of that function. Because the next LargeInt function that will be called will be the construction or assignment of RetVal, this function will see that flag.

In another embodiment of the invention, a recursive divide-and-conquer algorithm is used for the divide operator. The following explanation uses the following notation for a divide operation:

divide (LValue, RValue) returns (Result, Remainder)

5   *LValue* is the dividend, *RValue* is the divisor, *Result* is the quotient, and *Remainder* is the remainder.

The first part of the algorithm is to adjust the signs of the operands and results so that the division operation is performed on positive numbers. In addition,   if (LValue < RValue) then the algorithm returns (0, LValue).

10   The second part of the algorithm begins with recognition of the fact that:

LValue = Result * RValue + Remainder (eq. 1)

From this, LValue is redefined as:

LValue = UpperValue * Power + LowerValue (eq. 2)

UpperValue can be viewed as a selected group of the MSBs, and LowerValue can 15 be viewed as the remaining LSBs. The value of Power is selected as a power of 2 so that the multiplication is achieved by shift operations. For example, if Power is $2^{\wedge}32$, then:

UpperValue = LValue >> 32

and

20   LowerValue = LValue [31 downto 0]

The next part of the algorithm involves recursion. First, the UpperValue is divided by RValue:

Divide(UpperValue, RValue)

which returns (UpperResult, UpperRemainder).

25   Substituting for UpperValue in equation 2 yields:

LValue = (UpperResult * RValue + UpperRemainder) * Power +
LowerValue   (eq. 3a)

Rearranging the preceding equation yields:

LValue = (UpperResult * Power) * RValue + (UpperRemainder *
30   Power + LowerValue) (eq. 3b)

Then the algorithm recursively divides the lower part of the LValue:

Divide (UpperRemainder * Power + LowerValue, RValue)

which returns (LowerResult, LowerRemainder).

LValue can now be reexpressed as:

$$LValue = (UpperResult * Power) * RValue + (LowerResult * RValue + LowerRemainder)(eq. 4a)$$

5   which is rearranged as:

$$LValue = (UpperResult * Power + LowerResult) * RValue + LowerRemainder \quad (eq. 4b)$$

The required format for the final answer is:

$$Result = UpperResult * Power + LowerResult$$

10   $$Remainder = LowerRemainder$$

In order to effectively divide the work between the two recursive-divide branches, the number of bits that represent UpperValue and the number of bits that represent (UpperRemainder * Power + LowerValue) are selected to be equal. Recall that the two recursive divide branches are:

15   $$Divide (UpperValue, RValue) \text{ returns } (UpperResult, UpperRemainder)$$

and

$$Divide (UpperRemainder * Power + LowerValue, RValue) \text{ returns } (LowerResult, LowerRemainder)$$

20   The following description explains how a value for Power is selected in each level of the recursion. The description denotes the number of bits used to represent X by NumBits(X). MaxNumBits(X) means that although NumBits(X) may vary, NumBits(X) will always be less than or equal to MaxNumBits(X). *Power* is 2 raised to some power (exponent), and PowExp refers to the exponent.

25   Assuming that each bit in X is an independent, binary random variable with a 50% probability of being either a 1 or a 0, MaxNumBits(X) provides a reasonable estimation of NumBits(X). To begin,

$$NumBits(UpperValue) = NumBits(LValue) - PowExp \quad (eq. 5)$$

30   $$MaxNumBits(LowerValue) = PowExp$$

$$MaxNumBits(UpperRemainder) = NumBits(RValue)$$

$$MaxNumBits(UpperRemainder * Power) =$$

$$\text{NumBits(RValue)} + \text{PowExp}$$

$$\text{MaxNumBits(UpperRemainder * Power + LowerValue)}$$

$$= \text{NumBits(RValue)} + \text{PowExp} \quad \text{(eq. 6)}$$

Because it is required that:

5      $$\text{NumBits(UpperValue)} = \text{NumBits(UpperRemainder *}$$

$$\text{Power + LowerValue)}$$

then substituting equations 5 and 6 into that yields:

$$\text{NumBits(LValue)} - \text{PowExp} =$$

$$\text{NumBits(RValue)} + \text{PowExp}$$

10     $$\text{PowExp} =$$

$$\text{ceiling(NumBits(LValue)} - \text{NumBits(RValue))}$$

$$/\ 2$$

Thus, the optimum partitioning of the recursive divide between the upper and lower parts of the dividend is the ceiling of 1/2 the difference between the

15   number of bits in the dividend (LValue) and the number of bits in the divisor (RValue).

The base case for the recursive divide-and-conquer algorithm is when UpperResult = 0. If UpperResult = 0, then it is known that LValue/RValue < 2. This is because LValue and RValue differ in length by zero or one bit, and after

20   shifting LValue by the number of bits that represent Power in order to obtain UpperValue, UpperValue will be less than RValue, and the recursive divide returns (0, UpperValue). Because LValue/RValues < 2, the algorithm returns (1, LValue-RValue).

From the foregoing it will be appreciated that the base case in the

25   recursive divide-and-conquer algorithm performs a simple subtraction, and recombining the results in the recursion requires only additions and bit shifting. Thus, the division is accomplished with additions and shifting of bits. In addition, with each level of recursion, the value of Power is chosen by halving the difference ("bit difference") between NumBits(LValue) and NumBits(RValue).

30   Thus, the depth of the recursion tree is $\log_2$(bit difference). By comparison, a standard division algorithm, which shifts the divisor by 1 bit with each level, has a depth that is equal to the bit difference. Thus, the division algorithm of the

present invention is O (log (bit difference)) in a best case scenario. An added benefit is that the result and remainder are available at the same time instead of arriving at the value through separate computations.

The present invention has been described in terms of operations on integer data in the context of a specific implementation in the C++ programming language. Those skilled in the art will appreciated that interoperable floating-point and rational datatypes can be constructed using the LargeInts datatype as the exponent/mantissa and numerator/denominator as appropriate. In addition, the invention could be implemented in any of a variety of programming languages, including both object-oriented and non-object-oriented programming languages, without departing from the present invention as set forth in the following claims.